TPS activity 1:

1. The starting addresses of Text Segment and Data Segment is

Text segment:0x00004020

Data segment: 0x10010000

1. Different register, different commands and lengths of commands.
2. The difference is 32 bytes. There are 8 values in a single address each value stores 4 bytes
3. We have 8 columns per address location
4. For each Colum we have 4 bytes
5. m: .word 20 insert to line 3
6. address of m is #store at 0x10010004

address of n is #0x10010000

1. str: .asciiz “I love CSE31” #asciiz
2. Address plus value of +8 so it will be stored in

#store at 0x10010008 to 0x10010014

1. yes
2. #print str1 here its below the finish:addi function

la $a0, str1

li $v0,4

syscall

1. The computer will take address of variable and store that address in $t3

TPS Activity 2:

1. J
2. n: word 25
3. str1: .asciiz "Less\n"

str2: .asciiz "Less than or equal to\n"

str3: .asciiz "Greater than\n"

str4: .asciiz "Greater than or equal to\n"

1. li $v0 5

syscall

move $s1 $v0

1. slt $t0, $s1, $s0

bne $t0, $zero, LESS

1. on mips file